

SUBROUTN

01636900 9405 F2E4

P4MBX16 SUBR

```
01637000 */*****  
01637100 */** P4MBX16  
01637200 */**  
01637300 */**  
01637400 */**  
01637500 */**  
01637600 */**  
01637700 */**  
01637800 */**  
01637900 */**  
01638000 */**  
01638100 */**  
01638200 */**  
01638300 */**  
01638400 */**  
01638500 */**  
01638600 */**  
01638700 */**  
01638800 */**  
01638900 */**  
01639000 */**  
01639100 */**  
01639200 */**  
01639300 */**  
01639400 */**  
01639500 */**  
01639600 */**  
01639700 */**  
01639800 */**  
01639900 */**  
01640000 */**  
01640100 */**  
01640200 */**  
01640300 */**  
01640400 */**  
01640500 */**
```

DESCRIPTION - 8 X 16 MULTIPLY WITH 16 BIT RESULT

FUNCTION:

THIS IS EQUIVALENT TO MULTIPLYING A 16 BIT QUANTITY BY AN 8 BIT FRACTION WITH ROUNDING TO 16 BITS RESULT.

PARAMETERS : NONE

CALLING ARGUMENTS : A ACC= CONTAINS THE 8 BIT MULTIPLIER
X REG= CONTAINS ADDRESS OF 16 BIT MULTIPLICAND

RETURNING ARGUMENTS: A ACC= CONTAINS MS BITS OF 16 BIT RESULT
B ACC= CONTAINS LS BITS OF 16 BIT RESULT

REGISTERS MODIFIED : A,B AND CONDITION CODE REGISTER

LENGTH : 21 BYTES

EXECUTION TIME : 68 CYCLES

STACK SPACE USED : 4 BYTES

SUBROUTINES CALLED : NONE